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This is to certify that **Thripathi M P** has published a research paper entitled 'Design of Single-Phase Active Filter Using Multi-level Inverter' in the International Journal of Scientific Research in Computer Science, Engineering and Information Technology (IJSRCSEIT), Volume 2, Issue 7, September 2017.

This Paper can be downloaded from the following IJSRCSEIT website link

<https://ijsrcseit.com/CSEIT174444>

IJSRCSEIT Team wishes all the best for bright future

A handwritten signature in blue ink, likely belonging to the Editor in Chief.

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